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Applicant: Andrew Marshall

Art Unit: 2818

Serial No.: 10/626,438

Examiner: Thong Q. Le

Filed: 07/24/03

Docket: TI-34177

For: CIRCUIT FOR REDUCING STANDBY LEAKAGE IN A MEMORY UNIT

Commissioner of Patents
P.O. Box 1450
Alexandria, VA 22313-1450

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AMENDMENT

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

RECEIVED
CENTRAL MAIL CENTER

Applicant Andrew Marshall

Docket Number: TI-34177

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Serial No.: 10/626,438

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Examiner: Thong Q. Le

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FACSIMILE COVER SHEET

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<input type="checkbox"/> NEW APPLICATION	<input type="checkbox"/> EOT
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<input type="checkbox"/> CONTINUATION APP'N	<input type="checkbox"/> ELECTION
<input type="checkbox"/> DIVISIONAL APP'N	
NAME OF INVENTOR(S): Andrew Marshall	
RECEIPT DATE & SERIAL NO.: Serial No.: 10/626,438 Filing Date: 07/24/03	
TITLE OF INVENTION: Circuit for Reducing Standby Leakage in a Memory Unit	
TI FILE NO.: TI-34177	DEPOSIT ACCT. NO.: 20-0668
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Texas Instruments Incorporated
PO Box 655474, M/S 3998
Dallas, TX 75265-5474